### INTEGRATED CIRCUITS

# DATA SHEET

## **SA58603**

High precision operational amplifier, comparator, and voltage reference

Product data Supersedes data of 2002 Nov 13





## High precision operational amplifier, comparator, and voltage reference

### SA58603

#### **GENERAL DESCRIPTION**

The SA58603 is comprised of a low voltage, high precision dual operational amplifier, a comparator, and a reference voltage source. The input offset voltage is typically 100  $\mu V$  with a very low temperature drift of  $\pm 1~\mu V/^{\circ} C$ . The SA58603 supply current is typically 100  $\mu A$  per amplifier and it operates from 1.8 V to 6 V single supply.

Having single power supply capability, low current consumption, low offset voltage, low input offset current and low input bias current, the SA58603 is ideal for battery-powered applications and amplification of very small signals. It is excellent for precision amplifiers in gas burners and gas pilot water heaters which use thermal couple heat sensors.



#### General

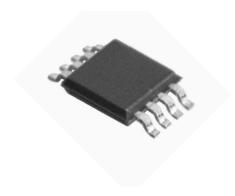
- Functionality to 1.8 V typical
- Low supply current: 100 μA per amplifier (typical)

#### **Amplifier section**

- Very low input offset voltage: 100 μV (typical)
- $\bullet$  Very low input offset drift:  $\pm 1~\mu\text{V/}^{\circ}\text{C}$  (typical)
- Low input offset current: 1 nA (typical)
- Input bias current: 50 nA (typical)
- Open loop gain: 100 dB (typical)
- Common mode input includes ground

#### Comparator section

- Input offset voltage: ±1.0 mV (typical)
- Low input offset voltage voltage drift (-40 to +85 °C): ±10 μV (typical)
- Input bias current: 25 nA (typical)
- Output sink current: 5 mA (minimum)



#### Reference voltage section

- Reference voltage: 1.27 V ±50 mV
- Reference voltage temperature characteristics: ±100 ppm/°C (typical)
- Output current: 0.3 mA (minimum)

#### **APPLICATIONS**

- Gas burners
- Gas water heaters
- Tankless gas water heaters

#### SIMPLIFIED DEVICE DIAGRAM

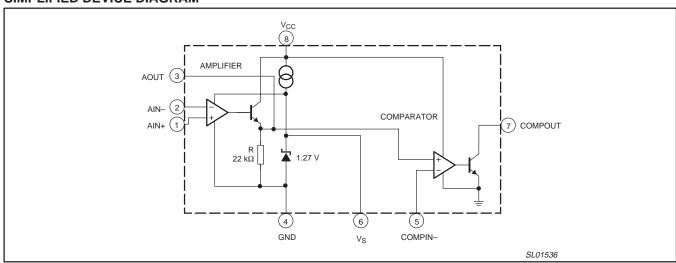


Figure 1. Simplified device diagram.

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#### **ORDERING INFORMATION**

TYPE NUMBER	PACKAGE	CKAGE				
I TPE NUMBER	NAME	DESCRIPTION	VERSION	RANGE		
SA58603D	SO8	olastic small outline package; 8 leads; body width 3.9 mm SOP005		–40 to +85 °C		

### **PIN CONFIGURATION**

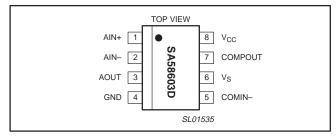


Figure 2. Pin configuration.

#### **PIN DESCRIPTION**

PIN	SYMBOL	DESCRIPTION
1	AIN+	Non-inverting input of Op Amp. This is a PNP amplifier with the reference voltage as its supply voltage.
2	AIN-	Inverting input of Op Amp. This is a PNP amplifier with the reference voltage as its supply voltage.
3	AOUT	Output of Op Amp. This is a NPN emitter follower output with a 22 k $\Omega$ internal on-chip pull-down resistor. Its supply voltage is $V_{CC}$ .
4	GND	Ground.
5	COMPIN-	Inverting input of Comparator. This is a PNP amplifier with V <sub>CC</sub> as its positive supply voltage.
6	V <sub>S</sub>	Reference voltage output. The output reference is typically 1.27 V. The output reference is derived from the series combination of a NPN transistor (configured as a diode with the base shorted to the collector) and a 120 k $\Omega$ resistor.
7	COMPOUT	Output of Comparator. It is an open collector output stage which requires an external pull-up resistor.
8	V <sub>CC</sub>	Positive supply. Its operating range is 1.8 to 6.0 V.

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### **EQUIVALENT CIRCUITS**

PIN NUMBER	PIN NAME	INTERNAL EQUIVALENT CIRCUIT	PIN NUMBER	PIN NAME	INTERNAL EQUIVALENT CIRCUIT
1	AIN+	V <sub>S</sub> R 25 kΩ  R 18 kΩ	5	COMPIN-	V <sub>S</sub> R 43 kΩ  R 39 kΩ
2	AIN-	V <sub>S</sub> R 25 kΩ  R 18 kΩ	6	V <sub>S</sub>	V <sub>CC</sub> R 120 kΩ
3	AOUT	V <sub>CC</sub> R 22 kΩ	7	COMPOUT	V <sub>CC</sub> R 47 kΩ
4	GND		8	V <sub>CC</sub>	

### **MAXIMUM RATINGS**

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
V <sub>CC</sub>	Single supply voltage	-0.3	+10	V
V <sub>IN</sub>	Input voltage	-0.3	+10	V
T <sub>stg</sub>	Storage temperature	-40	+125	°C
T <sub>amb</sub>	Operating temperature		+85	°C
Р	Power dissipation	-	300	mW

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#### **ELECTRICAL CHARACTERISTICS**

 $V_{CC}$  = 3.0 V,  $V_{IN}$  = 0 V, and  $T_{amb}$  = 25  $^{\circ}C,$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CC</sub>	Supply current		-	0.1	0.15	mA
I <sub>OVS</sub>	Reference voltage output current		0.3	-	-	mA
Vs	Reference voltage		1.22	1.27	1.32	V
$\Delta V_S$	Reference voltage temperature drift		-	±100	-	ppm/°C
PSRR	Power supply rejection ratio	f = 100 Hz	50	60	-	dB
V <sub>CC</sub>	Power supply voltage operating range		1.8	3.0	6.0	V
Amplifier s	section (note 1)					
V <sub>INA</sub>	Input voltage range		-0.2	-	0.3	V
V <sub>IOA</sub>	Input offset voltage		_	±0.1	±0.35	mV
$\Delta V_{IO}/\Delta T$	Input offset voltage temperature drift	$T_{amb} = -20 \text{ to } +75 ^{\circ}\text{C}$	-	±1	±3	μV/°C
I <sub>IOA</sub>	Input offset current		-	1	10	nA
I <sub>i(bias)A</sub>	Input bias current		-	50	150	nA
G <sub>v(ol)A</sub>	Open-loop voltage gain		80	100	_	dB
I <sub>OA</sub>	Output current	V <sub>IN</sub> = 10 mV; V <sub>O</sub> = 0.5 V	0.5	_	_	mA
V <sub>OA</sub>	Output voltage swing	$V_{IN} = -5 \text{ to } +25 \text{ mV}; R_L = 10 \text{ k}\Omega$ 0		-	V <sub>CC</sub> – 1.0	V
Comparato	or section (note 2)					
V <sub>IOC</sub>	Input offset voltage	$V_{IN} = -5 \text{ mV}$	-	±1.0	±3.5	mV
$\Delta V_{IOC}/\Delta T$	Input offset voltage temperature drift	$V_{IN} = -5 \text{ mV}$	-	±10	±30	μV/°C
I <sub>i(bias)C</sub>	Input bias current	$V_{IN} = -5 \text{ mV}$	-	25	75	nA
I <sub>O(sink)</sub>	Output sink current	$V_{IN} = 10 \text{ mV}; V_{O} = 0.4 \text{ V}$	5	-	-	mA
I <sub>LO</sub>	Output leakage current	V <sub>O</sub> = V <sub>CC</sub> + 1 V	-	-	0.2	μΑ
V <sub>SAT</sub>	Output saturation voltage	$V_{IN} = 10 \text{ mV}; I_{O(sink)} = 5 \text{ mA}$	_	200	400	mV

Amplifier output is emitter follower with an on-chip 22 kΩ pull-down resistor.
 Comparator output is open collector; it requires an external pull-up resistor. See application section on how to determine this resistor.

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#### TYPICAL CHARACTERIZATION CURVES

 $T_{amb}$  = 25 °C,  $V_{CC}$  = 3 V,  $V_{IN}$  = 0 V, unless otherwise specified.

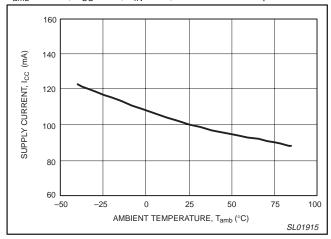


Figure 3. Supply current versus ambient temperature.

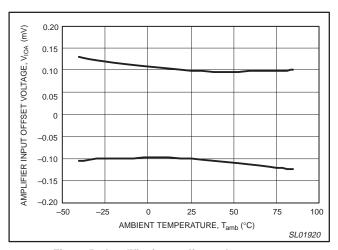


Figure 5. Amplifier input offset voltage versus ambient temperature.

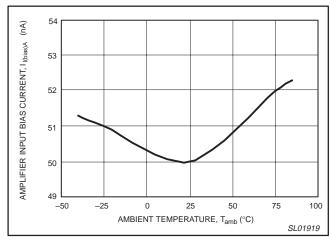


Figure 7. Amplifier input bias current versus ambient temperature.

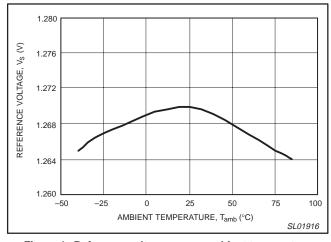


Figure 4. Reference voltage versus ambient temperature.

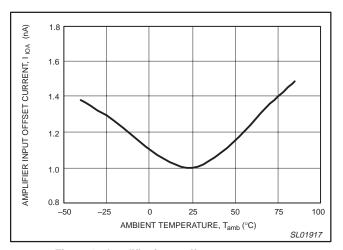


Figure 6. Amplifier input offset current versus ambient temperature.

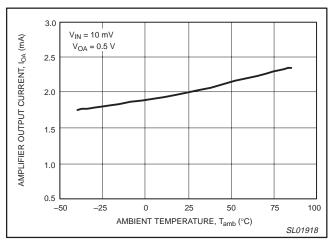


Figure 8. Amplifier output current versus ambient temperature.

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#### **TYPICAL CHARACTERIZATION CURVES** (continued)

 $T_{amb}$  = 25 °C,  $V_{CC}$  = 3 V,  $V_{IN}$  = 0 V, unless otherwise specified.

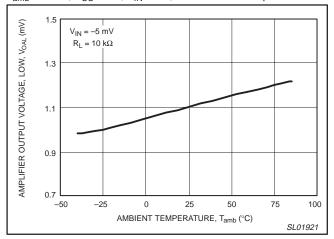


Figure 9. Amplifier output voltage, LOW versus ambient temperature.

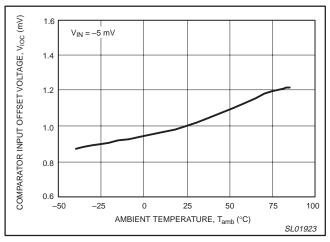


Figure 11. Comparator input offset voltage versus ambient temperature.

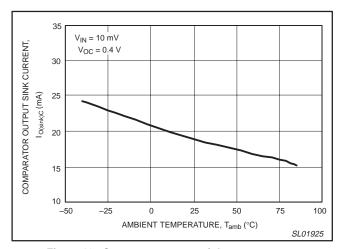


Figure 13. Comparator output sink current versus ambient temperature.

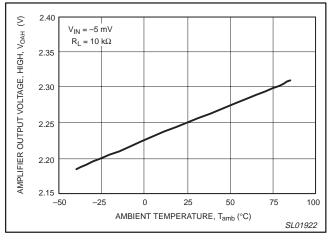


Figure 10. Amplifier output voltage, HIGH versus ambient temperature.

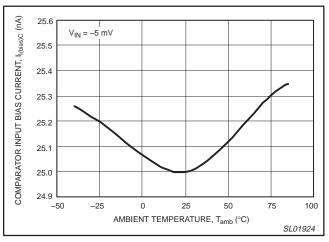


Figure 12. Comparator bias current versus ambient temperature.

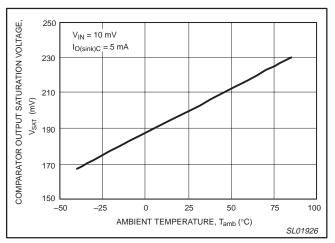


Figure 14. Comparator output saturation voltage versus ambient temperature.

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#### TYPICAL CHARACTERIZATION CURVES (continued)

 $T_{amb}$  = 25 °C,  $V_{CC}$  = 3 V,  $V_{IN}$  = 0 V, unless otherwise specified.

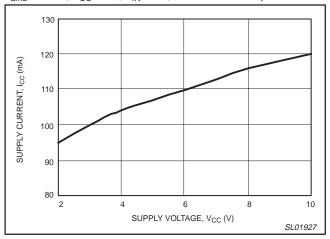


Figure 15. Supply current versus supply voltage.

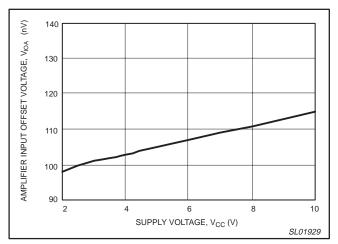


Figure 17. Amplifier input offset voltage versus supply voltage.

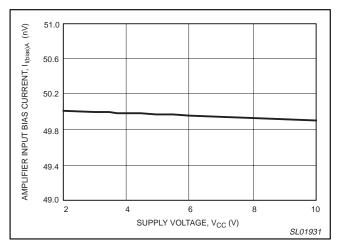


Figure 19. Amplifier input bias current versus supply voltage.

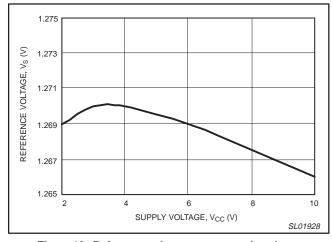


Figure 16. Reference voltage versus supply voltage.

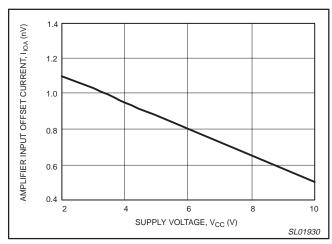


Figure 18. Amplifier input offset current versus supply voltage.

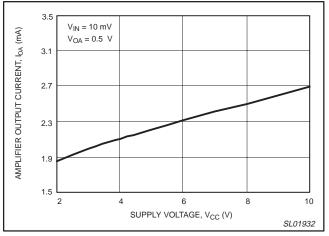


Figure 20. Amplifier output current versus supply voltage.

#### TYPICAL CHARACTERIZATION CURVES (continued)

 $T_{amb}$  = 25 °C,  $V_{CC}$  = 3 V,  $V_{IN}$  = 0 V, unless otherwise specified.

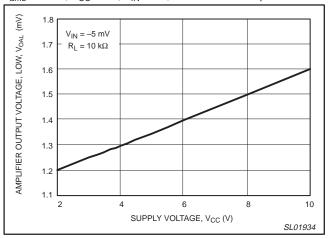


Figure 21. Amplifier output voltage, LOW versus supply voltage.

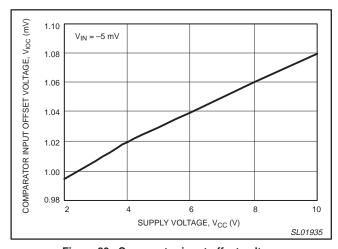


Figure 23. Comparator input offset voltage versus supply voltage.

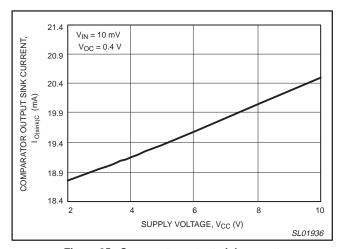


Figure 25. Comparator output sink current versus supply voltage.

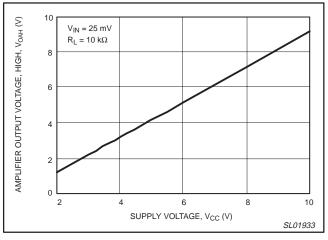


Figure 22. Amplifier output voltage, HIGH versus supply voltage.

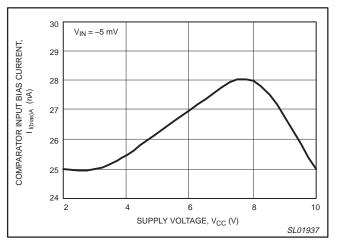


Figure 24. Comparator bias current versus supply voltage.

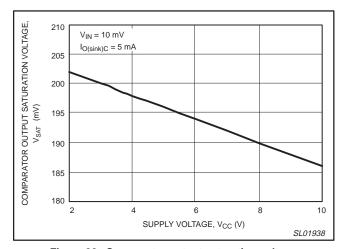


Figure 26. Comparator output saturation voltage versus supply voltage.

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#### **TYPICAL CHARACTERIZATION CURVES** (continued)

 $T_{amb}$  = 25 °C,  $V_{CC}$  = 3 V,  $V_{IN}$  = 0 V, unless otherwise specified.

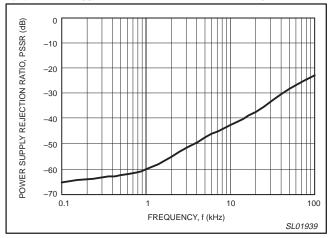


Figure 27. Power supply rejection ratio versus frequency.

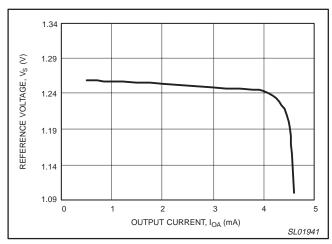


Figure 29. Reference voltage versus output current.

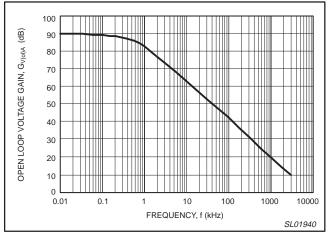


Figure 28. Amplifier open loop gain versus frequency.

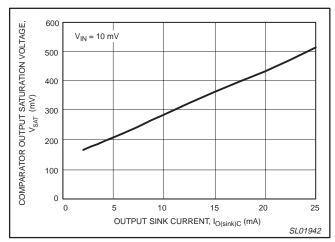


Figure 30. Comparator output saturation voltage versus sink current.

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#### APPLICATION INFORMATION

#### Typical gas pilot detection circuit

Figure 31 shows a typical application of the SA58603 in which the input amplifier is driven by a thermocouple heated by the pilot flame in a water heater or gas burner. The circuit comparator may be programmed to detect when the pilot is out. The IC is operated from a single voltage supply making it capable of battery backup with loss of AC power. It may be operated from a one-cell Lithium battery or two alkaline cells. 10 nF bypass capacitors are placed from  $V_{CC}$  and reference voltage output (pin 6) to ground to filter power supply noise and interference from external noise sources.

#### Selecting external components

#### Gain

Recommended amplifier gain is 40 dB. The gain is set by the combination of R1, R2, and R3 as shown in the following equations:

$$\mathsf{Av} \,=\, \frac{(\mathsf{R3} \,+\, \mathsf{R2})}{\mathsf{R3}}\,;\, \mathsf{Av}\; (\mathsf{dB}) = 20\; \mathsf{log}\; (\mathsf{Av});\, \mathsf{R3} = \mathsf{R1} \parallel \mathsf{R2}$$

For a gain of 40 dB, Av = 100 V/V;

if R2 = 1 k
$$\Omega$$
, then 99\*R3 = 1 k $\Omega$ ; R3 = 10.1  $\Omega$ .

R3 = 
$$\frac{R1 \times R2}{(R1 + R2)}$$
; R3 × R1 + R3 × R2 = R1 × R2:  
R3 × R2 = R1 (R2 - R3)

Thus R1 = 
$$\frac{R3 \times R2}{(R2 - R3)}$$
 = 10.2 $\Omega$ 

#### Comparator output pull-up resistor

The comparator output pull-up resistor is determined by the following formula:

$$R = \frac{\left(V_{CC} - V_{SAT(max)}\right)}{I_{O(sink)(min)}}$$

where  $V_{SAT(max)} = 400 \text{ mV}$ , and  $I_{O(sink)(min)} = 5 \text{ mA}$ .

#### Comparator threshold voltage

The comparator threshold voltage is determined by R4 and R5. They form a voltage divider from the reference voltage output ( $V_S$ , Pin 6) to ground. In the application example,  $V_{COMP}$ , the comparator input (COMPIN–, Pin 5) is connected to the junction of R4 and R5. The input level is calculated by following relationship:

$$V_{COMP} = 1.27V \times \frac{R5}{(R4 + R5)}$$

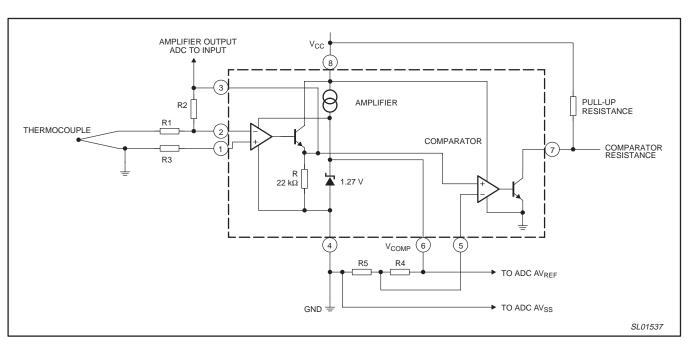


Figure 31. Typical flame detection circuit.

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#### **PACKING METHOD**

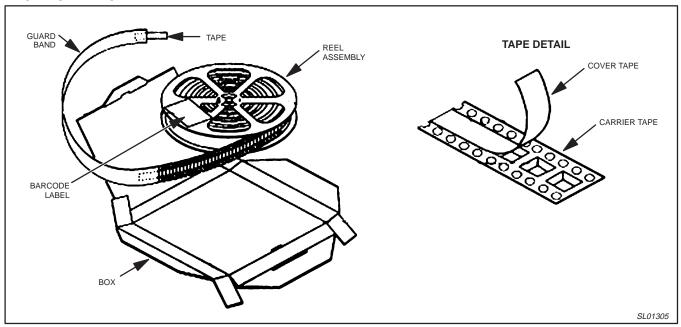
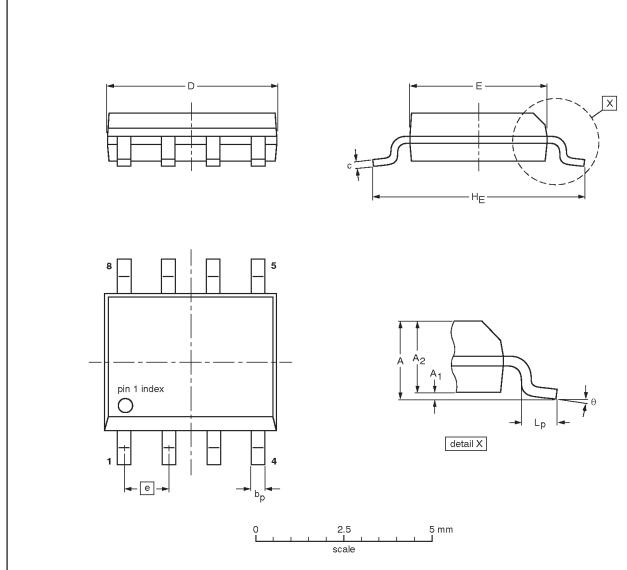


Figure 32. Tape and reel packing method.

#### SO8: plastic small outline package; 8 leads; body width 3.9 mm

**SOP005** 



#### DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A <sub>1</sub>	A <sub>2</sub>	bp	С	D <sup>(1)</sup>	E <sup>(2)</sup>	е	HE	Lp	θ
mm	1.73	0.25 0.10	1.45 1.25	0.51 0.33	0.25 0.19	4.95 4.80	4.0 3.8	1.27	6.2 5.8	1.27 0.38	8°
inches	0.068	0.010 0.004	0.057 0.049	0.013 0.020	0.0100 0.0075	0.20 0.19	0.16 0.15	0.050	0.244 0.228	0.050 0.015	0°

#### Notes

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	RENCES	EUROPEAN ISSUE DA		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE	
SOP005	076E03	MS-012			03-10-07	

# High precision operational amplifier, comparator, and voltage reference

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### **REVISION HISTORY**

Rev	Date	Description
_3	20031110	Product data (9397 750 12287). ECN 853–2289 30322 of 09 September 2003. Supersedes data of 2002 Nov 13 (9397 750 10671).
		Modifications:
		Change package outline version to SOP005 in Ordering information table and Package outline sections.
_2	20021113	Product data (9397 750 10671). ECN 853–2289 29151 of 05 November 2002. Supersedes data of 2001 Oct 03 (9397 750 08955).
_1	20011003	Product data (9397 750 08955). ECN 853–2289 27197 of 03 October 2001.

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#### Data sheet status

Level	Data sheet status [1]	Product status <sup>[2] [3]</sup>	Definitions
I	Objective data	Development	This data sheet contains data from the objective specification for product development.  Phillips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
III	Product data	Production	This data sheet contains data from the product specification. Philips Semiconductors reserves the right to make changes at any time in order to improve the design, manufacturing and supply. Relevant changes will be communicated via a Customer Product/Process Change Notification (CPCN).

<sup>[1]</sup> Please consult the most recently issued data sheet before initiating or completing a design.

#### **Definitions**

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information — Applications that are described herein for any of these products are for illustrative purposes only. Philips Semiconductors make no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

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Date of release: 11-03

Document order number: 9397 750 12287

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<sup>[2]</sup> The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.

<sup>[3]</sup> For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.